

Today's Ethernet

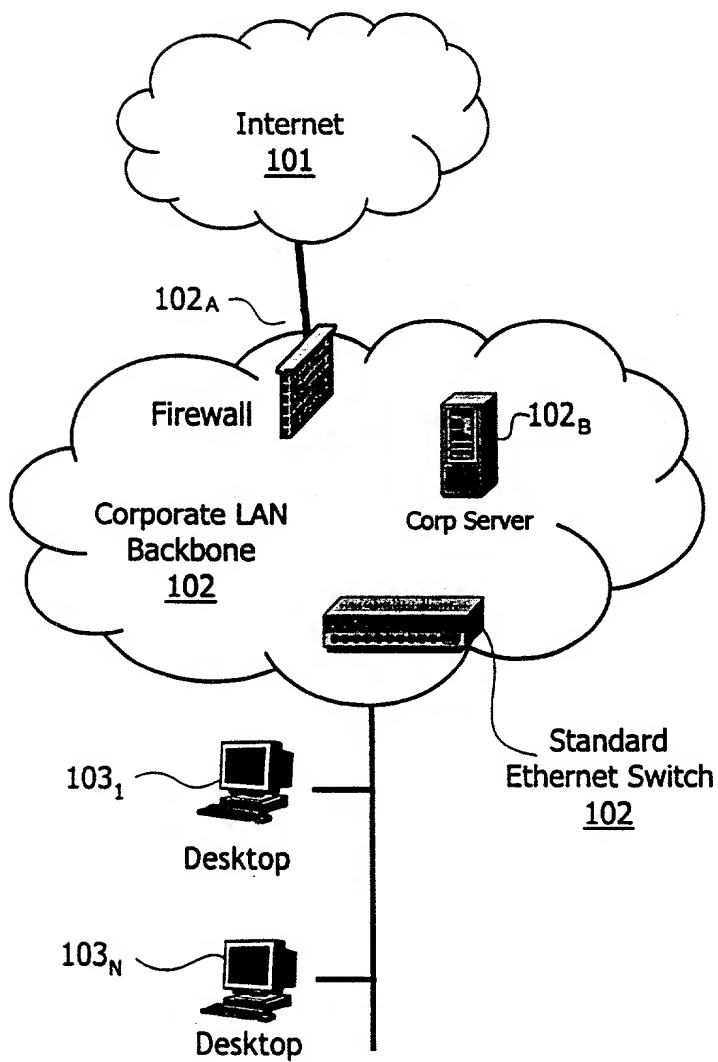


FIG. 1

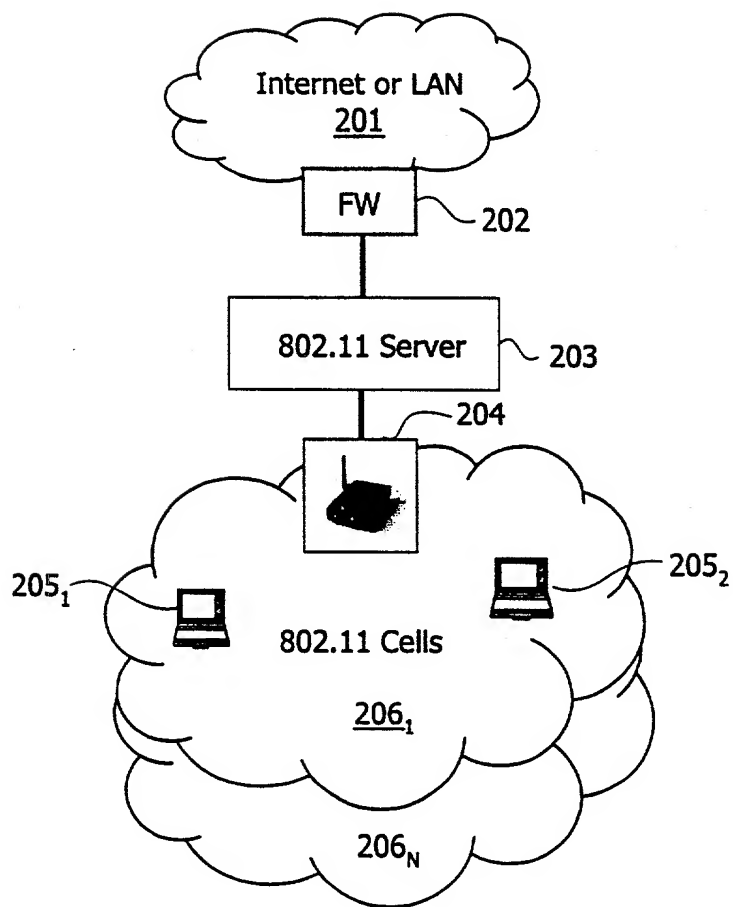


FIG. 2

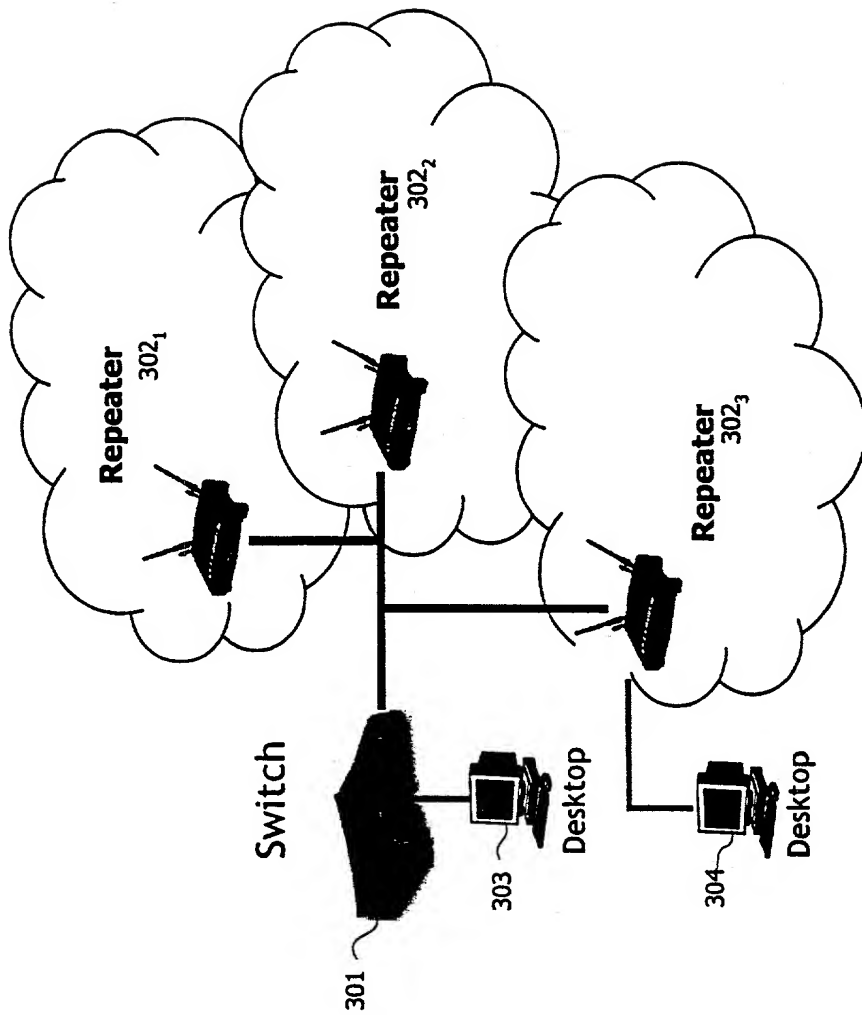


FIG. 3

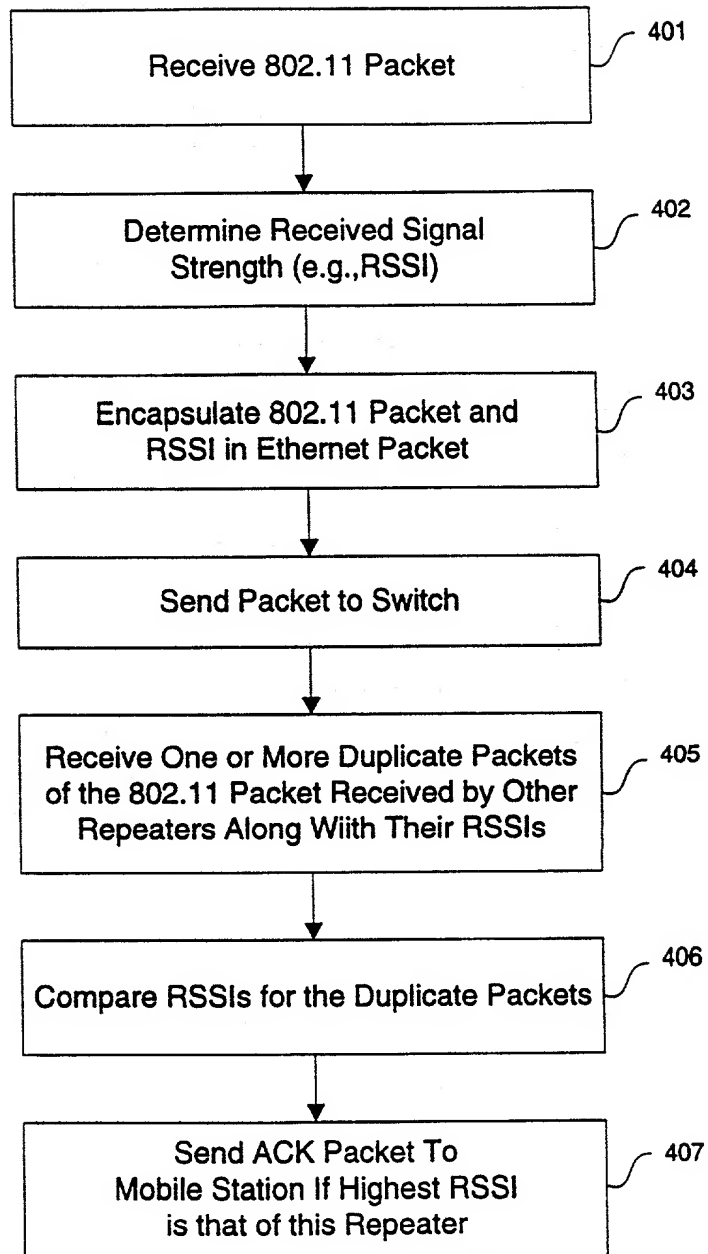


FIG. 4A

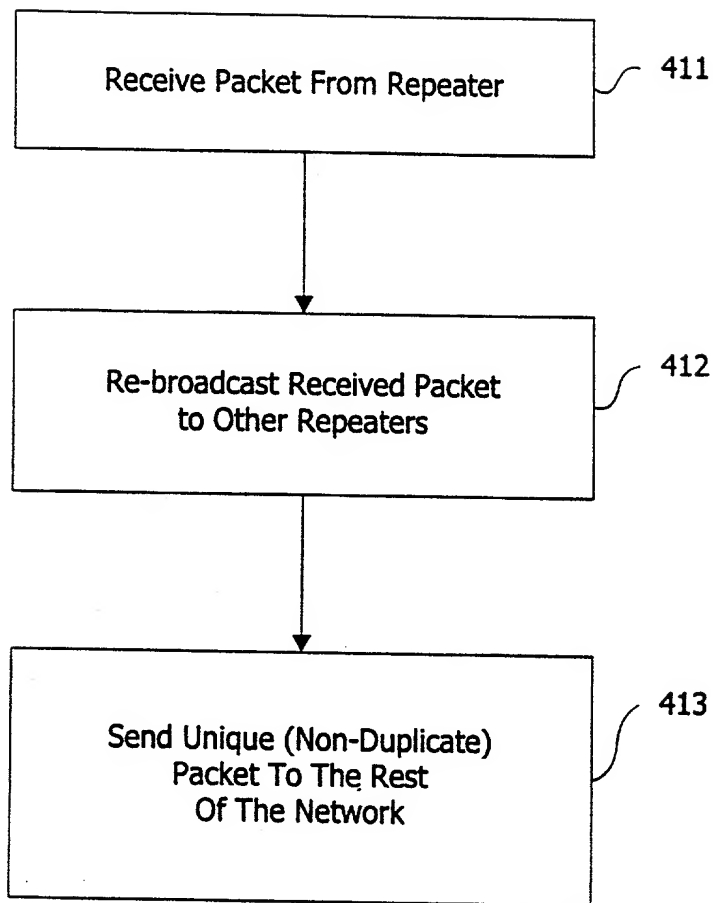


FIG. 4B

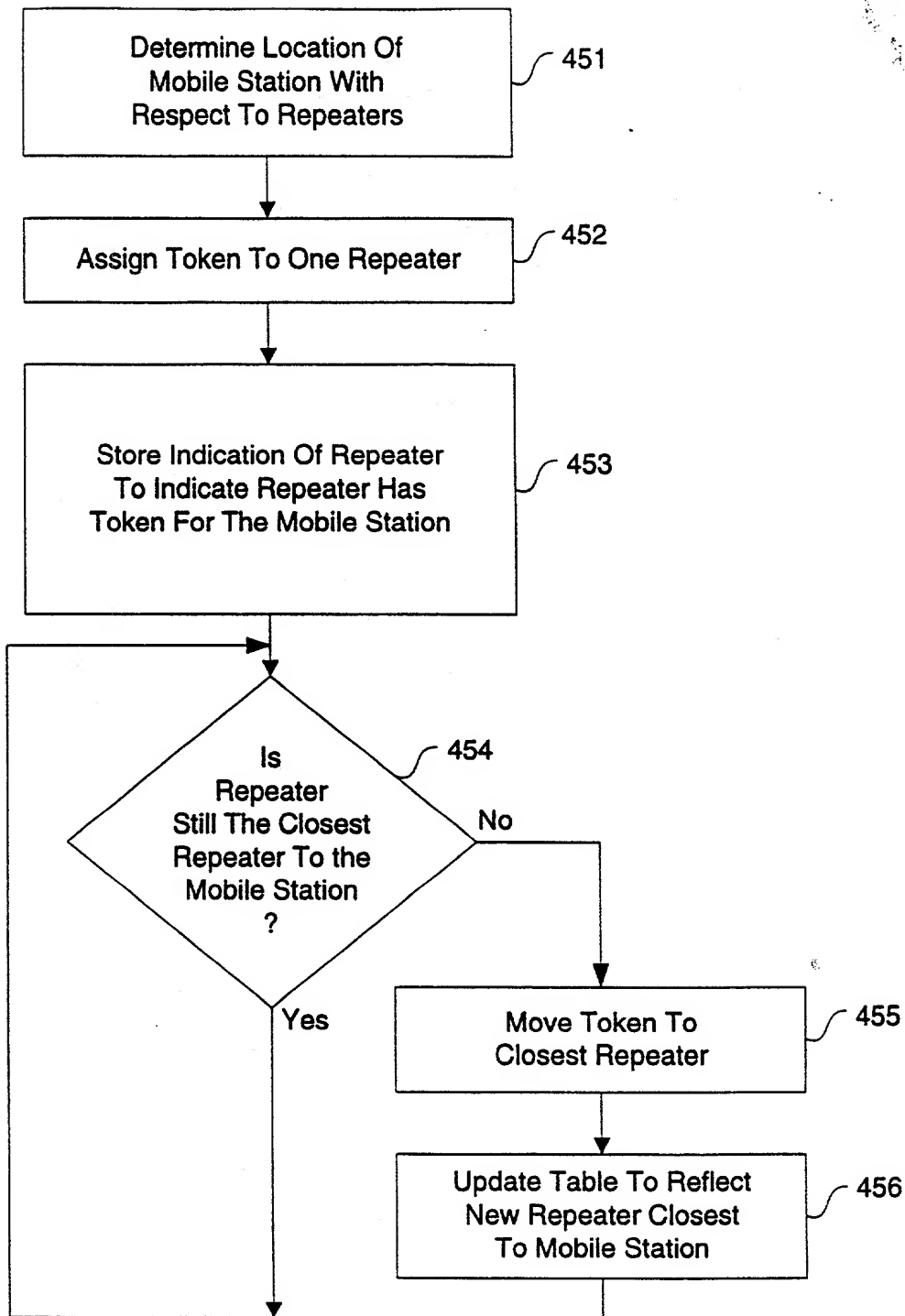


FIG. 4C

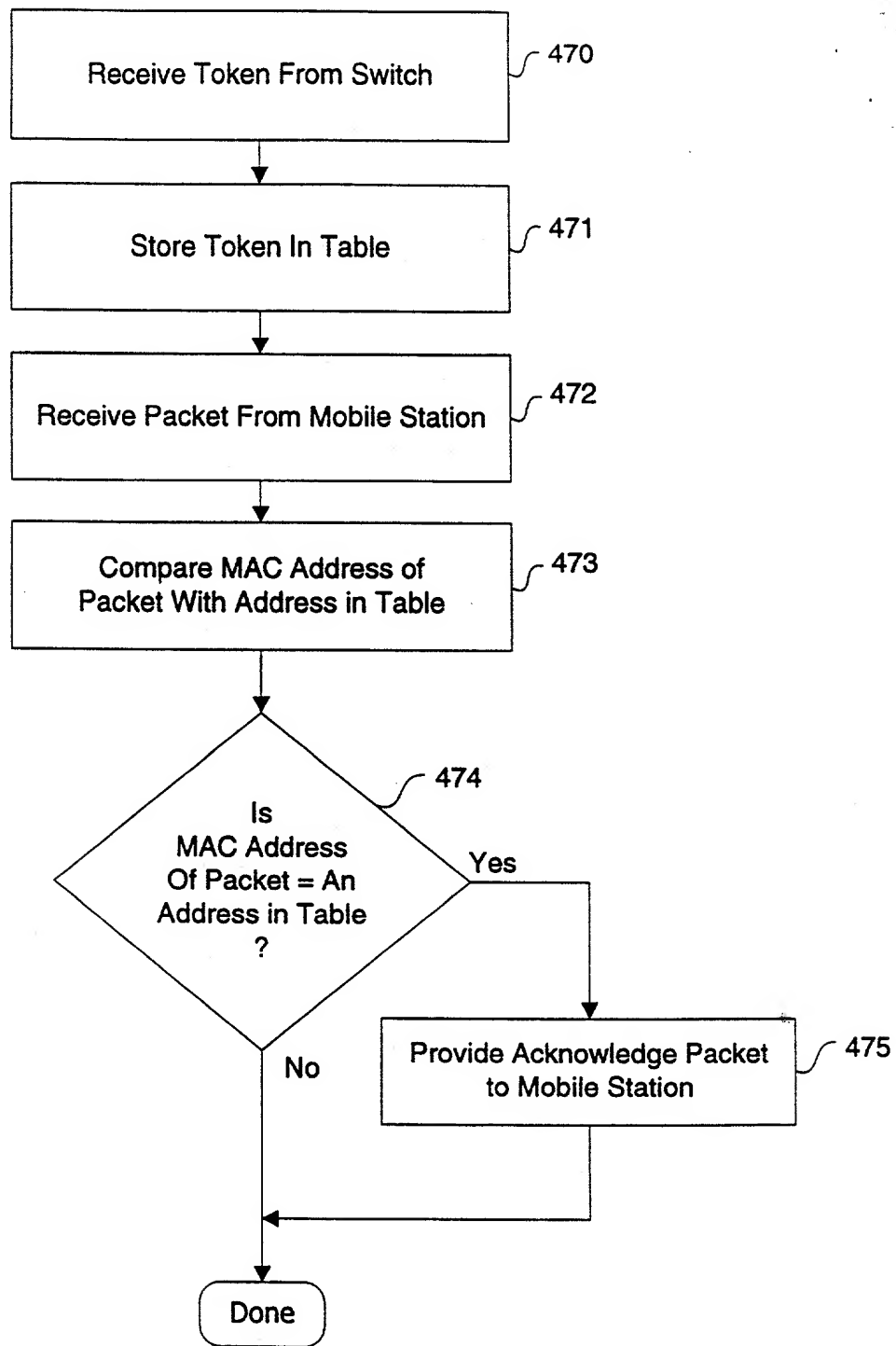


FIG. 4D

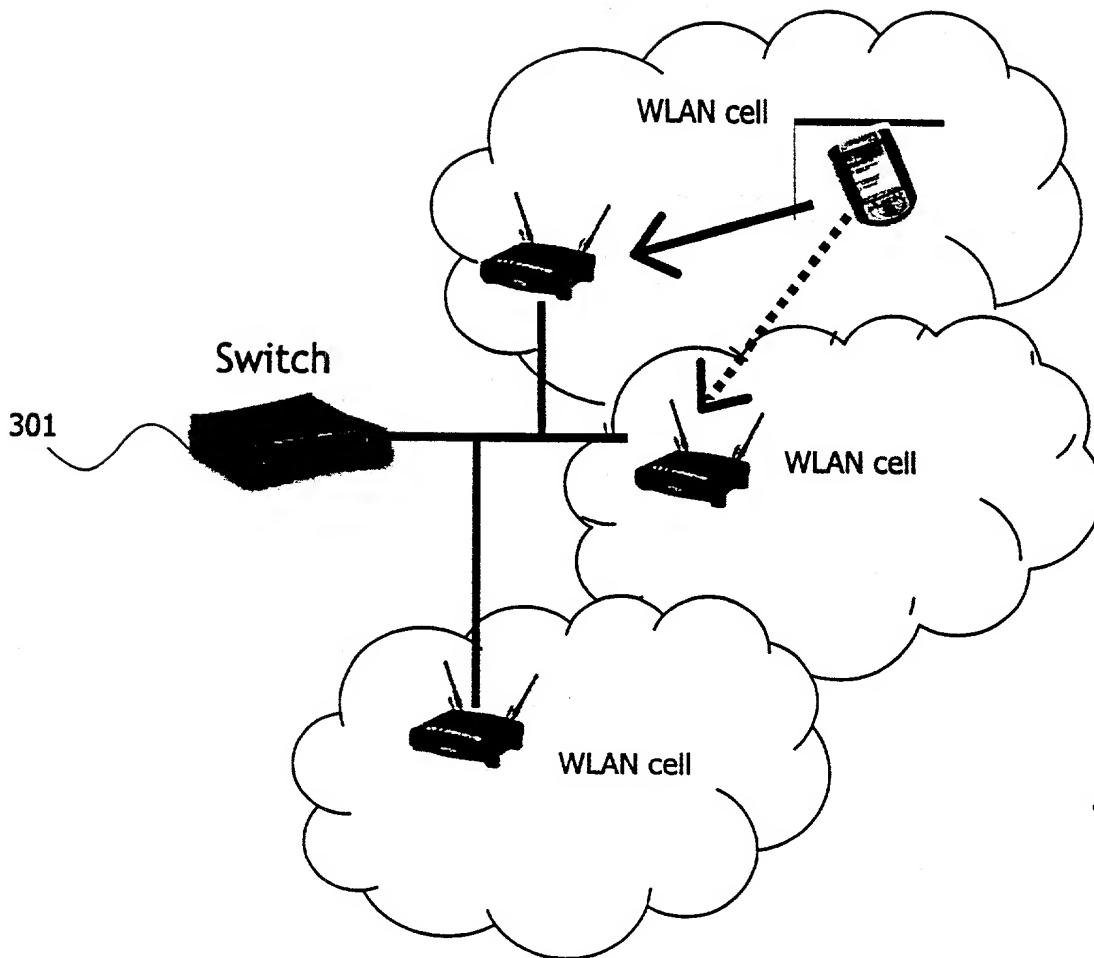


FIG. 5A

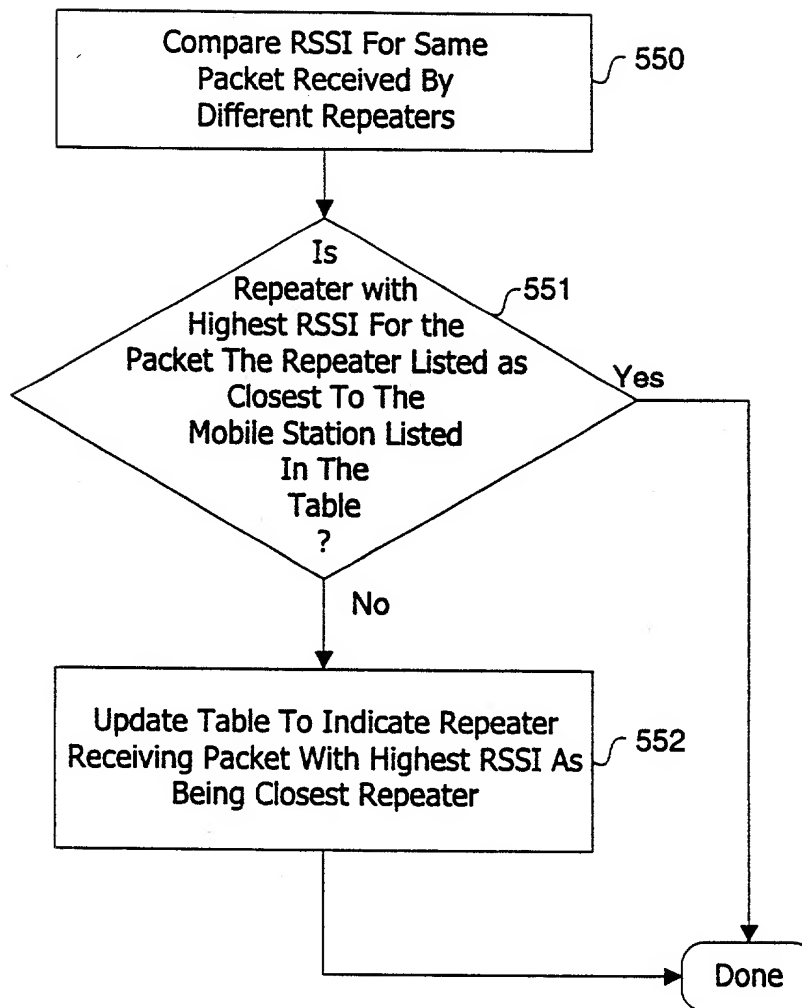


FIG. 5B

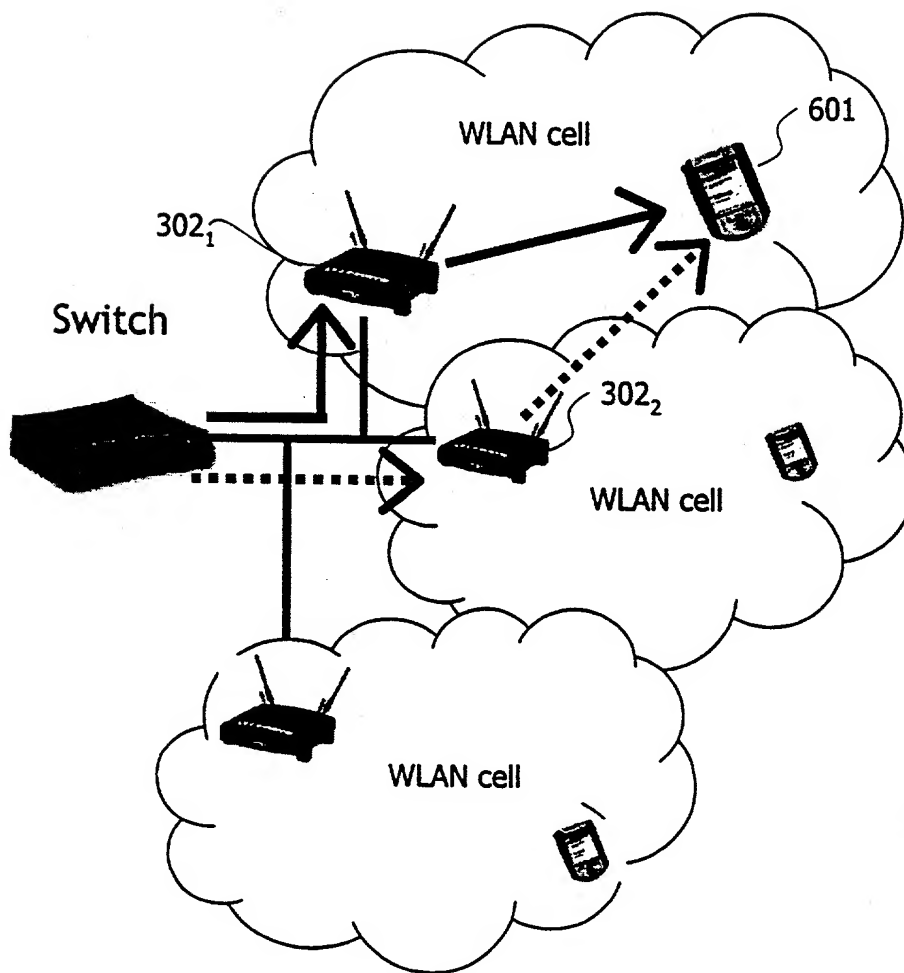


FIG. 6A

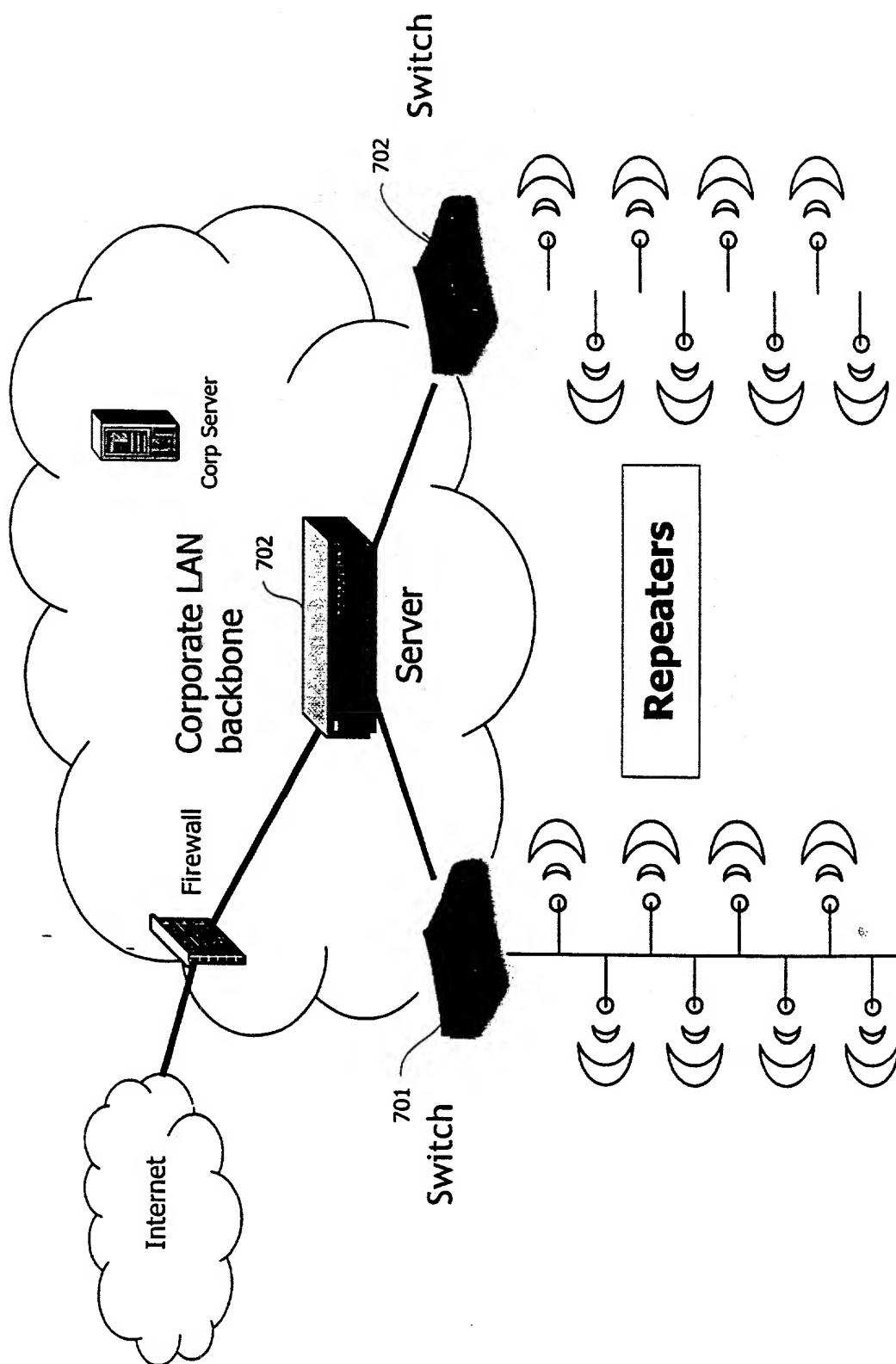
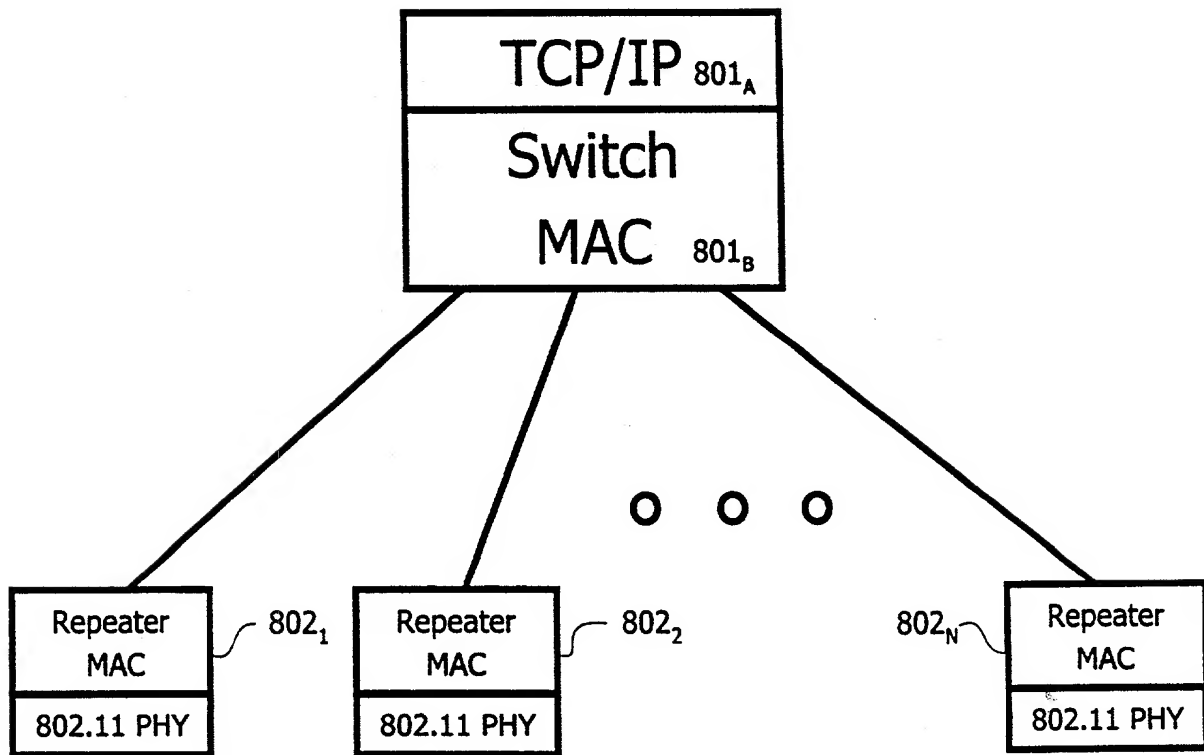


FIG. 7

Switch
802



Repeaters
802

FIG. 8

Switch

Session mgmt <u>901</u>	SNMP	<u>907</u>
802.1x, RADIUS, VPN <u>902</u>		
Location tracking <u>903</u>		
Fragmentation <u>904</u>		
DCF <u>905</u>		
Packet De-duplication		<u>906</u>

FIG. 9A

Repeater

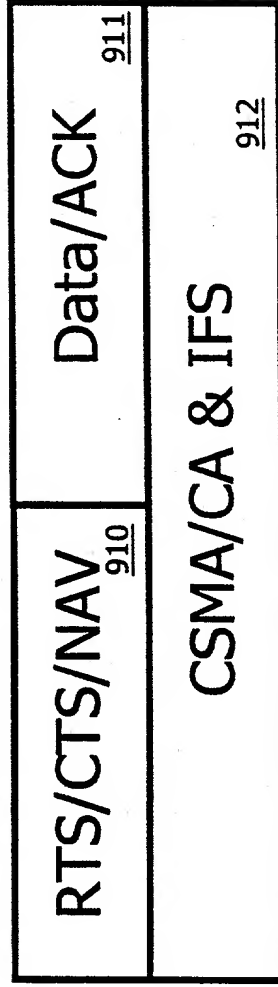


FIG. 9B

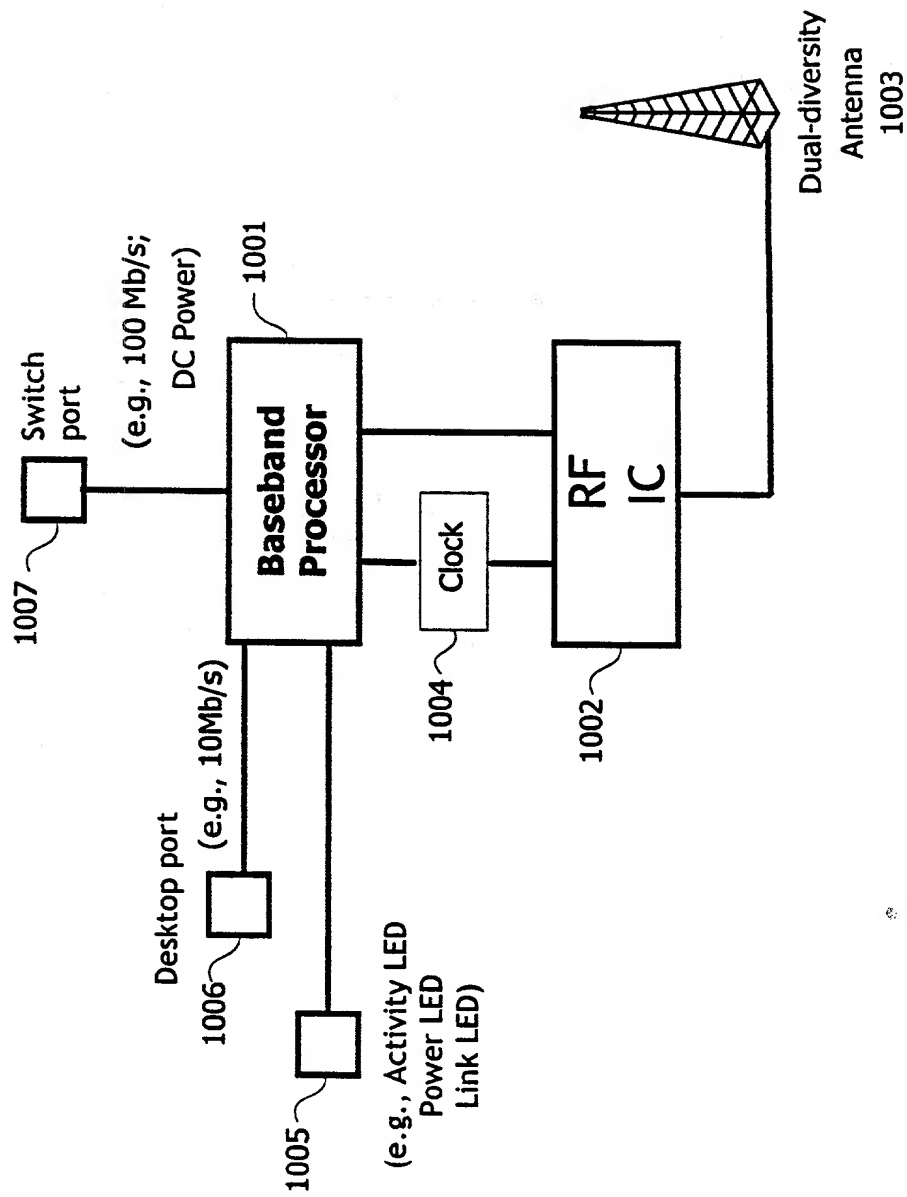


FIG. 10

FIG. 11 is a block diagram of a Baseband Processor 1001. The Baseband Processor 1001 includes a Dual-diversity Antenna 1003, an RF IC 1002, a Repeater MAC/Control 1105, a Switch 1103, a 100bT MAC/PHY 1101, a Power Distribution 1102, and a 100bT MAC/PHY 1104. The Baseband Processor 1001 is connected to a Switch port, Activity/Power/Link LEDs 1005, and a Desktop port 1006. The Baseband Processor 1001 also includes a TCP/IP 802.11 MAC 802.11 PHY block and a TCP/IP 802.11 MAC 802.11 MAC Tunnel block.

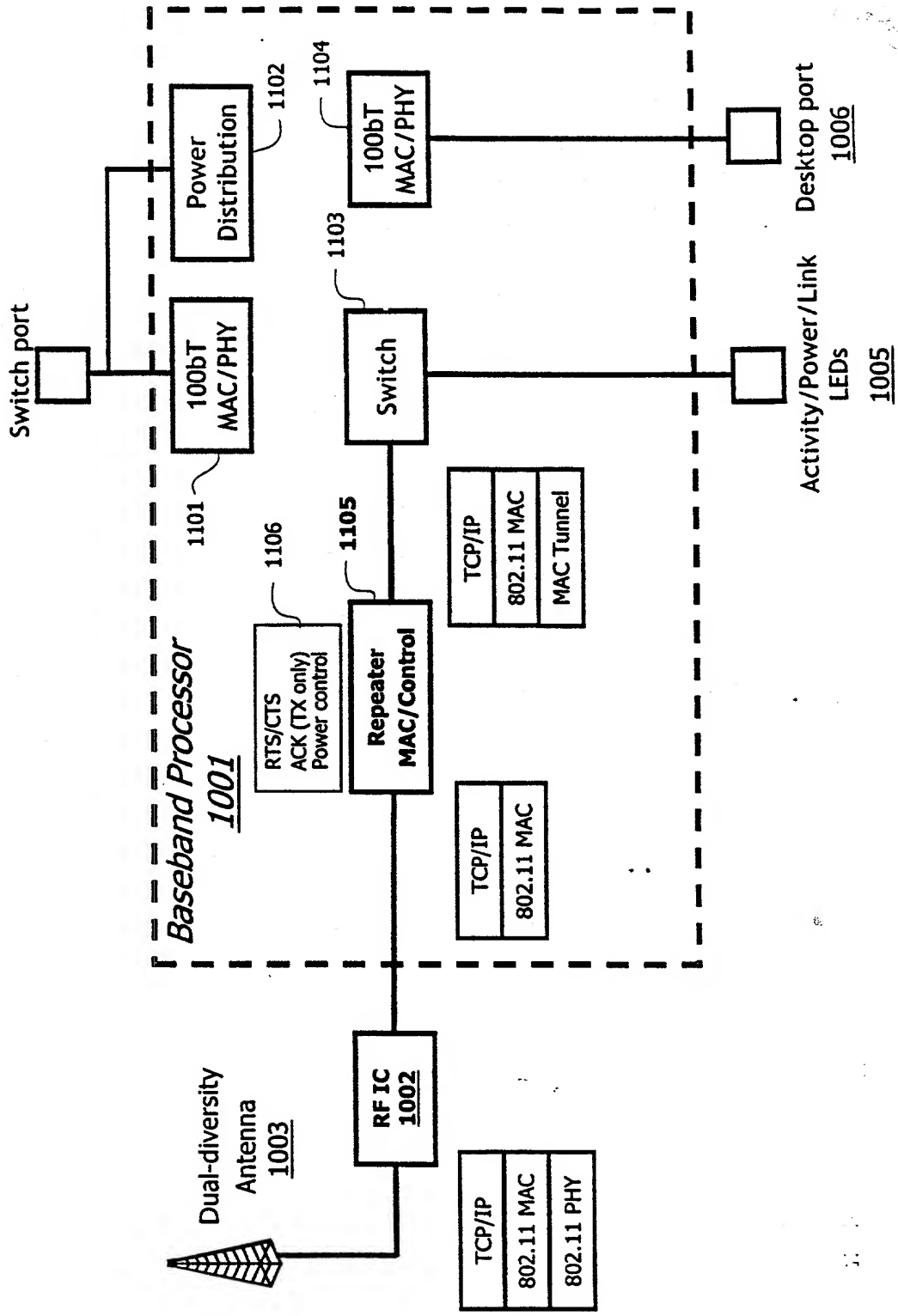


FIG. 11

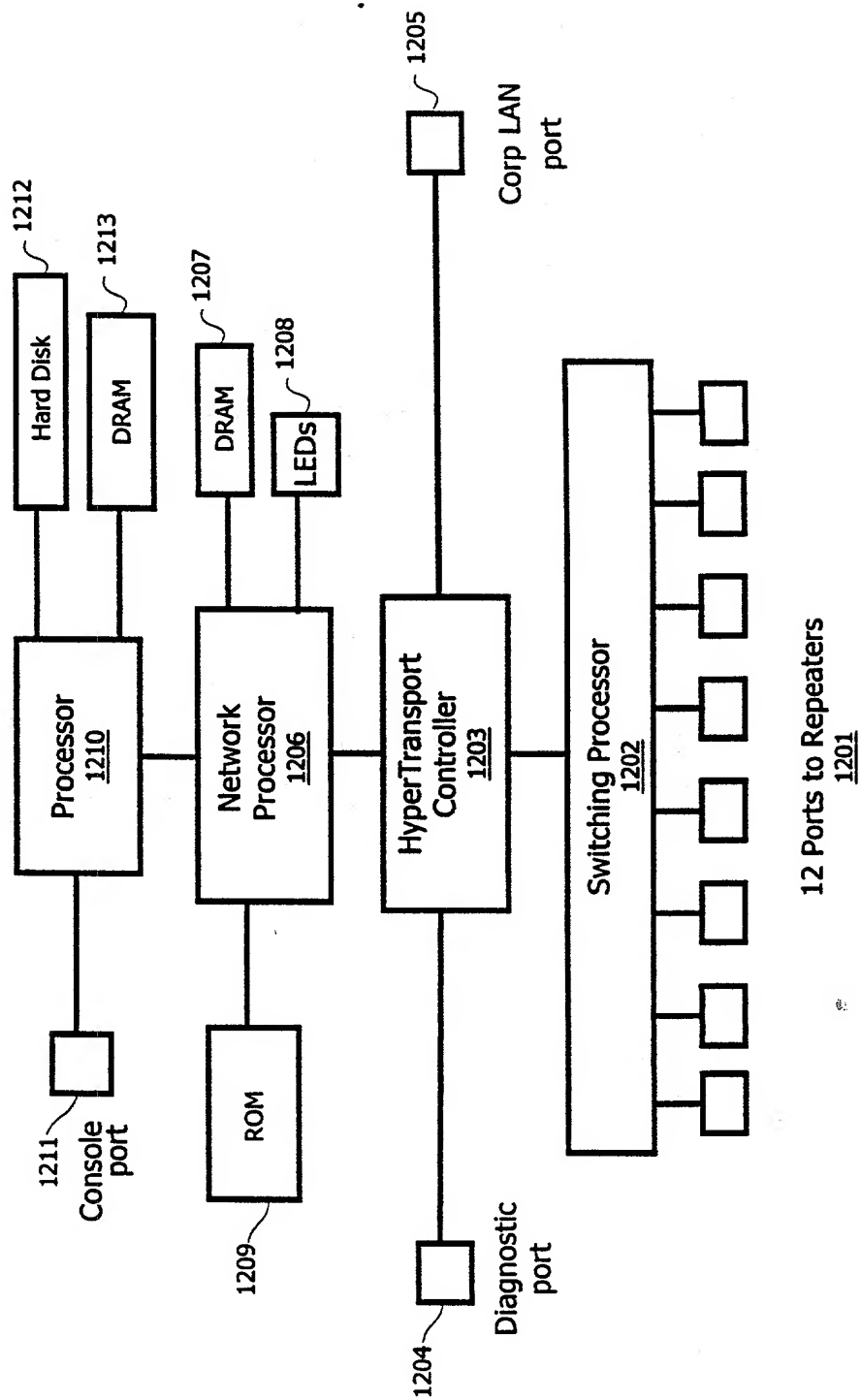


FIG. 12